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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/972,855	10/10/2001	Yoshiaki Sugizaki	04329.2686	5564
22852	7590 05/12/2006		EXAMINER	
FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER			IM, JUNGHWA M	
LLP 901 NEW YC	ORK AVENUE, NW		ART UNIT	PAPER NUMBER
WASHINGTON, DC 20001-4413			2811	

DATE MAILED: 05/12/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

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Applicant(s)

	09/972,855 SUGIZAKI, YOSHIAKI		SHIAKI
Office Action Summary	Examiner	Art Unit	
. •	Junghwa M. Im	2811	
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the o	correspondence	address
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply If NO period for reply is specified above, the maximum statutory period we Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	within the statutory minimum of thirty (30) day rill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed /s will be considered tin the mailing date of this ED (35 U.S.C. § 133).	
Status	,	•	
 1) ⊠ Responsive to communication(s) filed on 22 Fe 2a) ☐ This action is FINAL 2b) ⊠ This 3) ☐ Since this application is in condition for alloware closed in accordance with the practice under E 	action is non-final. nce except for formal matters, pro		he merits is
Disposition of Claims			
4) ☐ Claim(s) 25 is/are pending in the application. 4a) Of the above claim(s) is/are withdraw 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 25 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or			
Application Papers			
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) access Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Examine 11.	epted or b) objected to by the drawing(s) be held in abeyance. Se ion is required if the drawing(s) is ob	e 37 CFR 1.85(a). ojected to. See 37	CFR 1.121(d).
Priority under 35 U.S.C. § 119			
a) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the prior application from the International Bureau * See the attached detailed Office action for a list of	s have been received. s have been received in Applicat ity documents have been receiv ı (PCT Rule 17.2(a)).	ion No ed in this Nation	al Stage
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 9/2003. 5. Patent and Trademark Office	4) Interview Summary Paper No(s)/Mail D 5) Notice of Informal I 6) Other:		PTO-152)

Application No.

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DETAILED ACTION

Continued Examination Under 37 CFR 1.114

A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on February 22, 2006 has been entered.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claim 25 is rejected under 35 U.S.C. 103(a) as being unpatentable over Gurtler (US 5424245) in view of Bertin et al. (US 5977640), hereinafter Bertin.

Regarding claim 25, Fig. 1 of Gurtler shows a semiconductor device comprising: a first semiconductor chip (21) where a semiconductor element (22, 24, 32, 34) is formed;

a plurality of first connecting terminals (20) arranged on a semiconductor element formation surface side in the first semiconductor chip and connected electrically to the semiconductor element, the first connecting terminal having substantially the same configuration;

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a plurality of conductive members (28) buried in a plurality of through holes that go through the first semiconductor chip;

a plurality of second connecting terminals (36) on a back surface side of the semiconductor element formation surface side in the first semiconductor chip, and connected electrically to the semiconductor element via the conductive member,

a second semiconductor chip (10) stacked on the first semiconductor chip (21);

a plurality of the third connecting terminals (18) on a semiconductor element formation surface side (14, 16) in the second semiconductor chip,

wherein one of the first connecting terminals and the second connecting terminals of the first semiconductor chip is arranged at a position facing the third connecting terminals of the second semiconductor chip,

the first semiconductor chip and the second semiconductor chip are electrically connected with each other through the facing connecting terminals, and

some (48, 50) of the first connecting terminals or the second connecting terminals are distributed and arranged substantially on an entire surface of the semiconductor chip to receive a power source supply potential or ground potential.

Gurtler shows the most aspect of the instant invention except having a larger second semiconductor chip and a heat radiating plate thereon. Fig. 7 of Bertin shows a multi-chip packaging device wherein the second semiconductor chip (30) is larger than the first semiconductor chip (40) and a heat radiating plate (74) thereon.

It would have been obvious to one of ordinary skill in the art at the time of the invention to incorporate the teachings of Bertin to the device of Gurtler in order to have the second

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semiconductor chip larger than the first semiconductor chip for a compact package and a heat radiating plate thereon for the heat dissipation of the package.

Response to Arguments

Applicant's arguments with respect to the pending claims have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Junghwa M. Im whose telephone number is (571) 272-1655. The examiner can normally be reached on MON.-FRI. 8:30AM-5:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie C. Lee can be reached on (571) 272-1732. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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